JinPyo Kim

Seongnam, Korea | jinpyo.kim@sk.com | receipt of Ph.D. program fellowship from SK Hynix Inc.(2021 ~)

Education	**Graduate School of Sogang University**, Seoul, KOREA

Graduate School of Sogang University, Seoul, KOREA
M.S. at the Department of Computer Science and Engineering   Mar. 2014 - Feb. 2016
   Advisor: Professor Juho Kim
Sogang University, Seoul, KOREA
B.S. at the Department of Computer Science and Engineering   Mar. 2007 - Feb. 2014

Research Interest

1. Scalable and efficient storage system for reducing carbon emissions.
2. Memory/storage hierarchy with proper emerging memory technologies.
3. Recoverable persistent memory for crash consistency

Research Experience

CAD & VLSI Research Lab, Seoul, Korea

Master's Thesis : [Efficient Flash Cache Management in Online Transaction Processing Server](#)
Project : Process variation and BTI-aware Static Timing Analysis for Samsung   Feb. '14 – Jan. '15

Work Experience

SK Hynix Inc. , Seongnam, Korea
Embedded Software Engineer   Feb. '17 – present
- Conducted development on Enterprise SSD feature such as Power Loss Protection and customizing QoS(Quality of Service).

Junior Embedded Software Engineer   Feb. '16 – Jan. '17
- Conducted research on workload of real SSD device, performance estimation, QoS simulation and competitor benchmarking.

Republic of Korea Navy
Computer Technician   Apr. '08 – Apr. '10
- maintained a variety of linux OS servers such as Solaris and Red Hat at Naval Headquarters

Patents

P1 - "Memory controller, memory system having the same, and method of operating the same."
U.S. Patent No. 2019/0163387.
P2 – "Memory system and operating method thereof."
U.S. Patent No. 2020/0126624.
P3 – "Data storage device, operation method thereof and controller therefor."
U.S. Patent No. 2020/0301586.
P4 – "Memory system, memory controller and method for operating memory controller."
U.S. Patent No. 2021/0133058.
P5 – "Memory system and method for operating memory controller included therein."
U.S. Patent No. 2022/0043601.
P6 – " Storage device and method of operating the same."
U.S. Patent No. 2022/0050604.

Honors & Awards	SK Hynix Inc. Ph.D. Program Fellowship in 2021
	SK Hynix Inc. Industrial Scholarship in 2012

Technical Skills

Programming Languages: C/C++, Python, Java, UNIX shell
Platform : Windows XP/7/8/10, Linux, UNIX server
Debugger : TRACE32 (hardware debugger for embedded systems such as ARM architecture)

References

Prof. Dr.Juho Kim (advisor)
Professor at Sogang University, Head of Synopsys R&D Center and Director of CAD & VLSI Lab, Rich Hall 906A, Sogang University, Seoul, Korea.
Phone:+82-2-706-3997. Email: jhkim@sogang.ac.kr

Dr. Dongyoung Seo (co-worker)
Principal Engineer at SK Hynix Inc., Project Leader of Client SSD team, 13 floor, SK-U tower, Seongnam, Korea.
Phone:+82-10-. Email: dongyoung.seo@gmail.com